



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**Diploma in Electrical Engineering**

**SEMESTER - V (2021-2024)**

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEE			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	DTEE502	Industrial Electronics	3	0	2	4	60	20	20	30	20	150
2	DTEE503	Estimating and Costing	3	0	0	3	60	20	20	0	0	100
3	DTEE504	Power System Operation and Protection	3	0	2	4	60	20	20	30	20	150
4	DTEE604	Energy Conservation and Management	3	0	0	3	60	20	20	0	0	100
5	DTEE507	Minor Project	0	0	4	2	0	0	0	60	40	100
6	DTEE508	Industrial Training	0	0	2	1	0	0	0	0	50	50
7		Generic Elective III	3	0	0	3	60	20	20	0	0	100
8	DTEE509	Computer Aided Electrical Drafting Laboratory	0	0	4	2	0	0	0	30	20	50
<b>TOTAL</b>			<b>15</b>	<b>0</b>	<b>14</b>	<b>22</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>150</b>	<b>150</b>	<b>800</b>

**Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;**

**\*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.**

**Chairperson**  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Chairperson**  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Controller of Examinations**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Joint Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

4/7/2023

**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore